

STAFF PARTICULARS

UPDATED FOR: FY 2012

Name :	Shane Todd
Lab / Program :	NanoE / GaN
Position :	Scientist I
RO & HOD :	Patrick Lo
Date Joined :	01/12/2010

SUMMARY OF IME EMPLOYMENT						
From (MM/YY)	To (MM/YY)	Lab/Prog	Position/ Scheme/ Grade	Job Scope / Deliverables		
				Public-Funded Projects / Role (PL, Co-PL, Team Member)	Industry Projects / Role (PL, Co-PL, Team Member)	Core / Primary Op Responsibilities *
06/11	Present	NanoE / GaN	Scientist I	GaN TSRP- 1) GaN Growth Co-PI 2) GaN PDK PI	NXP - Technical scope drafting and planning Azzurro - Technical scope drafting and planning Huawei – Technical scope drafting and planning	GaN MOCVD tool procurement, staff recruitment, process integration design, background research literature review, device design and simulation, measurement tool procurement, measurement setup, proposal writing, IP generation
12/10	06/11	SAM	Scientist I / RSE2	NEM Switch / Team Leader	Kodak / Technical scope drafting and planning, integration flow design, review fabrication results	Process Flow Design, Simulations, Modeling, Device Measurements, Layout, Fabrication, Project Planning, Paper Writing

* Please refer to attached for explanation/example.

SUMMARY OF PREVIOUS EMPLOYMENT				
From (MM/YY)	To (MM/YY)	Name of Employer	Position	Key Achievements
09/05	11/10	UCSB	Graduate Student Researcher	Fabricated high aspect ratio transmission lines on low resistivity silicon, derived analytical models for transmission line behavior, developed novel micromachining process for creating low-loss oxide dielectric, designed passive microwave circuits including hybrid couplers and broadband filters, awarded NDSEG Fellowship, published 3

(Please submit to HR once the above details are updated)

				journal papers
09/02	08/05	U Florida	Research Assistant	Designed, modeled, simulated, and fabricated MEMS micromirrors. Developed novel 3-D micromirror design. Developed analytical models for electrothermal micromirrors.
01/05	06/05	Vism Corp.	Research Scientist	Developed a design, model, and fabrication process for a Fabry-Perot optical spectrometer.
08/01	12/01	AMD	Process Engineer Intern	Quality control on wafers processed in the photolithography module.

NUMBER OF PUBLICATIONS AND PATENTS (Accumulative from Join Date)		
Total Number of Publications	1 st Author	1
	Co-Author	2
Total Number of IME Prestigious Publications	1 st Author	1
	Co-Author	0
Total Number of Patents Filed	1 st Inventor	0
	Co-Inventor	0